

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L5	340483	latch or flip-flop	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/06 09:01
L6	3237	(back-to-back or cross-coupl\$3) same inverter\$1 same (transistor\$1 or mos or mosfet or cmos or fet)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/06 09:03
L7	1821	5 and 6	USPAT	OR	ON	2005/09/06 09:05
L8	438	"327"/\$.ccls. and 7	USPAT	OR	ON	2005/09/06 09:05
L10	2929	(parasitic adj capacit\$4) and detect\$4 and prevent\$3 and discharg\$3	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/06 09:26
L11	33118	input near2 (capacitor or capacitance)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/06 09:29
L12	417	10 and 11	USPAT	OR	ON	2005/09/06 09:30
L13	253	12 and (fedback or feedback)	USPAT	OR	ON	2005/09/06 09:30